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Substitute for form 1449/PTO

INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use as many sheets as necessary)

Sheet	1	of	1
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Complete if Known

Application Number	10/666,353
Filing Date	09/19/2003
First Named Inventor	Chandramouli Visweswariah
Art Unit	2641
Examiner Name	
Attorney Docket Number	YOR920030403US1

U. S. PATENT DOCUMENTS

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Examiner
Signature

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of

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		Jess, J.A.G. et al. "Statistical Timing for Parametric Yield Prediction of Digital Integrated Circuits", Proc. Design Automation Conference, ACM,P.932-937	
		Nishimoto Shuji et al., "A Statistical Static Timing Analyzer for CMOS Combinatorial Circuits Considering Correlations Between Delays", Vol.100, No.475 (2000/11/23)pp.11-16	

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